



# SRI VENKATESWARA COLLEGE OF ENGINEERING (Autonomous)

Karakambadi Road, Opposite LIC Training Centre, Tirupati – 517 507.  
Accredited by NBA (B.Tech – CSE, ECE,EEE,Mech.,Civil and IT) & NAAC with ‘A’ Grade  
Approved by AICTE, New Delhi, Permanently Affiliated to JNTUA, Ananthapuramu.

## Department of ECE

<b>Faculty Pursuing Ph.D (2021-22)</b>					
<b>S No.</b>	<b>Reg. No.</b>	<b>Name of the Faculty</b>	<b>Research Area of Ph.D work</b>	<b>Name of the University</b>	<b>Status</b>
1	19PH0401	Mr.Sk.Allabaksh	A real time low altitude aerial view image stitching with standalone devices	J.N.T.U.A. Ananthapuramu	Ongoing
2	14022P0439	Mr.A.Krishna Mohan	A directional edge based object tracking system	J.N.T.U.K. Kakinada	Ongoing
3	16PHD0192	Mr. K.Upendra Raju	Reversible Data Hiding Techniques in Encrypted domain in image processing	V.I.T.University, Vellore	Ongoing
4	15022P0430	B.Chandra Kala	Image Processing	J.N.T.U.K. Kakinada	Ongoing



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3	16PHD0192	Mr. K.Upendra Raju	Reversible Data Hiding Techniques in Encrypted domain in image processing	V.I.T.University, Vellore	Ongoing
4	R13409	Mrs. B.Sudha Rani	Performance Enhancement of Approximate Wallace Tree Multipliers by using Reversible logic gates in exact Compressor Adders	S.V.University, Tirupati	Ongoing
5	1203PH0878	Mrs. G.Sujatha	Optimization techniques for Efficient VLSI circuit design	J.N.T.U.H. Hyderabad	Ongoing
6	15022P0430	B.Chandra Kala	Image Processing	J.N.T.U.K. Kakinada	Ongoing



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### Ph.D guidance by the faculty (2021-22)

S.No.	Name of the student	Reg. No.	Name of the Guide	Research Area of Ph.D work	Year of Registration &(FT/PT)	Name of the University	Status of Ph.D work
1	Mullakuri Anusha	21BF1P0401	Dr.D.Srinivasulu Reddy	Image Processing	2021 & Full-Time	J.N.T.U.A., Ananthapuramu	On-going
2	D.Nagendra Babu	21RD2P0420	Dr.K.Lokesh Krishna	VLSI Design	2021 & Part-Time	J.N.T.U.A., Ananthapuramu	On-going
3	M.Kishor Babu	18PH0418	Dr.K.Lokesh Krishna	VLSI Design	2019 & Part-Time	J.N.T.U.A., Ananthapuramu	On-going
4	B.Doraswamy	16PH0408	Dr.K.Lokesh Krishna	VLSI Design and Embedded Systems	2017 & Part-Time	J.N.T.U.A., Ananthapuramu	On-going



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## Department of ECE

<b>Ph. D awarded during the assessment period (2021-22)</b>				
<b>S.No.</b>	<b>Name of the faculty</b>	<b>Date of award of Ph.D degree</b>	<b>Title of Ph.D degree</b>	<b>Name of the University Obtained</b>
1	Dr.G.Sujatha	04-03-22	Optimization techniques for Efficient VLSI circuit design	J.N.T.U.H. Hyderabad
2	Dr.B.Sudha Rani	10-12-21	Performance Enhancement of Approximate Wallace Tree Multipliers by using Reversible logic gates in exact Compressor Adders	S.V.University, Tirupati



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1	Dr.G.Padma priya	05-12-19	Digital Image Resolution and Contrast Enhancement improvement using advanced techniques	S.V.University, Tirupati